


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NITK- SURATHKAL
SHORT LISTED CANDIDATES FOR WRITTEN TEST & INTERVIEW
FOR PHD PROGRAMME June 2019 ADMISSION

Sl. No.	Application No.	Name of the Candidate
1.	P19EC0001	DASARI SRAVANTHI
2.	P19EC0002	SUNIL RATHORE
3.	P19EC0003	ATHULYA G
4.	P19EC0007	BINESH K
5.	P19EC0010	PUNEETH
6.	P19EC0012	SHERIN A THOMAS
7.	P19EC0020	CHAKALI PAKKIRIAH
8.	P19EC0022	SURAJ NEMINATH SHINDE
9.	P19EC0024	RAHUL E
10.	P19EC0028	PAMU HARIKA
11.	P19EC0035	ARATHI SANKAR P
12.	P19EC0039	SUMESH K P
13.	P19EC0040	MAYANK KUMAR SINGH
14.	P19EC0044	P B NATARAJAN
15.	P19EC0048	VIPIN SHARMA
16.	P19EC0052	SAI RAJANARENDRA
17.	P19EC0055	SRINIVASULU UTCHURU
18.	P19EC0064	TATA V SRINIVASA RAO
19.	P19EC0066	BADUGU VAMSI KRISHNA
20.	P19EC0067	RAVI KUMAR PALLAVOLU
21.	P19EC0069	SHASHIDHARA M
22.	P19EC0072	MIKRANT KUMAR
23.	P19EC0079	LAKSHMI NARAYANA MOGILI
24.	P19EC0082	M RAMESH NAIK
25.	P19EC0084	SANTOSH A JANAWADE
26.	P19EC0086	PAWANKUMAR B
27.	P19EC0088	MANCHALA NAVEEN
28.	P19EC0090	RASHMI BORAH
29.	P19EC0091	PITAMBARE SHWETA PREMCHAND
30.	P19EC0093	PADMA PRASADA
31.	P19EC0097	BANOTH THULASYA NAIK
32.	P19EC0100	SRIKANTH VEMULAPATI
33.	P19EC0101	YARRAGUNTLA SURESH BABU
34.	P19EC0106	RESHMA H
35.	P19EC0112	HANMANTH REDDY KOPPELA
36.	P19EC0114	S V RAVI KUMAR
37.	P19EC0122	BHARGAV NAGARAJU
38.	P19EC0123	BIPUL BORO


 HOD, E&C Engg. 04/06/19
 प्राध्यापक एवं विभागाध्यक्ष/PROF & HEAD
 इी एवं सी विभागाध्यक्ष/E & C Department
 एन् आई टी के, सुरतकल/NITK, Surathkal
 मंगलूर/MANGALORE - 575 025

1. Syllabus for Ph.D. admission test.

The test paper will have 3 parts. Part A is compulsory. The candidate can answer Part B or Part C depending on the area of interest. Each part has 15 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries - 0.25 marks.

Part A: Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

Part B: Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Signal Processing.


Part C: Linear and Digital IC's, Digital System Design, VLSI Technology, CMOS, VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

Place of Reporting : Dept. of Electronics and Communication Engg.

Written Test : 17th June, 2019, 9.00 AM - 10 AM

Document Verification : 17th June 2019, 10.15 AM onwards

Interview : 17th June 2019, 11 AM onwards (and to be continued on 18th June 2019 9.00 AM onwards, if required)


04/06/19
प्राध्यापक एवं सी. ई. टी. के. / PROF & HEAD
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