

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NITK- SURATHKAL

SHORT LISTED CANDIDATES FOR WRITTEN TEST & INTERVIEW
FOR MTECH (RESEARCH/SPONSORED) PROGRAMME - June 2019 ADMISSION

Sl. No.	App.No.	Name
1.	MR19CE0005 MR19VL0062	BIRADHER SHREYA
2.	MR19CE0089	AWATE REHAN SHAIKHARSHADHUSAIN
3.	MR19CE0098 MR19VL0096	SYED TAZMEERUL HASNAIN RIZVI
4.	MR19CE0108 MR19VL0107	PALASH GUPTA
5.	MS19CE0092	BOSETTY ARAVINDU
6.	MR19VL0006	VEMULA PRASHANTH
7.	MR19VL0009	AKSHAY ASHOK JAGAJAMPI
8.	MS19VL0017 MR19VL0116	ANJANASHREE M R
9.	MR19VL0018	GAJANANA R HEGDE
10.	MR19VL0036	ALOK PARMAR
11.	MR19VL0094	SURVE VAISHNAVI SUNIL
12.	MS19VL0109	ASHA ARAVATAGI


04/06/19
HOD, E&C Engg.
प्राध्यापक एवं प्रमुख/PROF & HEAD
ई एवं सी विभाग/E & C Department
एन् आई टी के, सुरतकल/NITK, Surathkal
मंगलूर/MANGALORE - 575 025

Syllabus for M.Tech by Research Admission test.

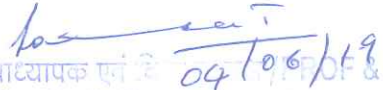
The Test paper will have 3 parts. Part A is compulsory. The candidate can answer Part B or Part C depending on the area of interest. Each part has 15 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries -0.25 marks.

Part A: Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

Part B: Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Signal Processing.

Part C: Linear and Digital IC's, Digital System Design, VLSI Technology, CMOS, VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

Place of Reporting	: Department of Electronics and Communication Engg.
Written Test	: 17th June, 2019, 9.00 AM - 10 AM
Document Verification	: 17th June 2019, 10.15 AM onwards
Interview	: 17th June 2019, 11 AM onwards


04/06/19
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