



Department of Electronics and Communication Engineering
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL
Under Ministry of Education (MoE), Government of India

Date: 5th May 2021

Corrigendum to Advertisement Date on 3th May 2021 for the post of JRF

Last date for submission of application for the post of JRF to work on the R&D project titled “**Design and Development of Nanoscale Integrated System Along with Conformal Antenna as Capsule Prototype for Wireless Capsule Endoscopy**” supported by Biomedical Technology Development and Transfer Division (BTDT), DST, Govt. of India, New Delhi collaboration with JNU New Delhi and AIIMS Delhi has been extended **up to 20th May 2021.**

Dr. Sandeep Kumar

Project Investigator and Assistant Professor,

Department of E & C Engg.

NITK, Surathkal, Mangaluru, Karnataka, India



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Advt. No.: TDP/BDTD/32/2021/General

Dated: 05/05/2021

Advertisement for the Position of JRFs

Applications are invited from the interested candidates for the post of JRFs to work on the R&D project titled “**Design and Development of Nanoscale Integrated System Along with Conformal Antenna as Capsule Prototype for Wireless Capsule Endoscopy**” supported by Technology Development and Transfer Division, DST, Govt. of India, New Delhi collaboration with JNU New Delhi and AIIMS Delhi. The selected candidate will be appointed initially for one year (on contract) and the services will be extended for next year based on the performance review.

| S No. | Position | Area of Specialization | Maximum Duration | Consolidated Salary | Number of Positions |
|-------|-----------------------------|------------------------|------------------|--|---------------------|
| 1 | JRF- Junior Research Fellow | Analog IC Design | 36 months | Rs. 31,000/- (Per month for first two years) Rs. 35,000/- (Per month for third year years plus HRA as per norms | One (01) |

Eligibility:

Educational Qualification:

BE/B.Tech in Electronics and Communication Engineering/Electronics Engineering and M. Tech./MS(R)/M.E./equivalent in Electronics and Communication Engineering/RF VLSI Design with expertise in RF and Analog IC Design. Candidates must have at least 7.5/10 CGPA or 60 percent marks in aggregate from a recognized institutes or universities (IITs/NITs/IIITs/Central Funded Institutes) as a full time program. **GATE/NET must be qualified.** A good GATE score will be an added advantage. Those who are appearing for M. Tech/ME last semester may also apply. Proof of M. Tech/ME certificate has to be provided during the time of interview.

Age: Not more than 28 year. Age relaxation will be given as per DST norms.

Note: - The candidate would be entitled to register for Ph.D. course at NITK Surathkal and JNU New Delhi



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Application Process:

Interested candidates must apply with the following documents: (1) Cover letter (2) Bio-data with passport-sized photograph, (3) Scanned copies of educational certificates and mark sheets, class XII onwards (4) GATE qualified certificate and (5) Scanned copies of Proof for research experience, special achievements and publications, if any. **The soft copies of all the above documents (pdf format) must be emailed to the Principal Investigator (Dr. Sandeep Kumar) on 20/05/2021 or before** and hard copies of all the documents should be send to the address for correspondence is given below. The email address for correspondence is given below. No TA/DA will be paid for attending the written test/interview. The appointment will be on a purely temporary basis co-terminus with the project. Candidates who are already employed should produce relieving certificate from their employers, if selected. **The shortlisted candidates with the date of written test/interview will be intimated by email only.**

Address for Correspondence:

To,
Dr. Sandeep Kumar
Co-Investigator and Assistant Professor
E&C Engineering Department
NITK Surathkal
Mangalore-575025
India.
Telephone: 0824-2473500
Email: sandeep@nitk.edu.in

NOTE: The envelope containing the application should be super scribed as “Application for the position of JRF under Technology Development and Transfer Division, DST project on “Design and Development of Nanoscale Integrated System Along with Conformal Antenna as Capsule Prototype for Wireless Capsule Endoscopy”.



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Application for the Position of Junior Research Fellow under SERB

1. Post Applied for: JRF-Junior Research Fellow
2. Name of the Candidate (BLOCKLETTER):
3. Father's Name (BLOCKLETTER):
4. Mother's Name (BLOCKLETTER):
5. (a) Date of Birth: (DD/MM/YYYY) _____
(b) Sex (Male/Female/Other): __ _
(c) Marital Status: Married/Single
(d) Category: SC/ST/OBC/PWD/Open
6. Previous Research experience: (use additional sheet if required) _____
7. Publication(s), if any: (use additional sheet if required) _____
8. GATE/ NET (if any): Qualified (Yes/No): Score: _____ Rank: _____
Specialization: _____ Year: _____
9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

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recent
Passport size
Photograph

| Name of Exam Passed | Name of the School/College/Institute/ University | Year of Passing | Discipline/ Specialization | Percentage of Marks/ CGPA |
|---------------------|--|-----------------|----------------------------|---------------------------|
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10. (a) Address for Communication: (BLOCKLETTER):

(b) Contact No (Mob):

(c) E-mail ID:

11. Contact Details of two referees:

| | Referee I | Referee II |
|----------------------|-----------|------------|
| Name : | | |
| Designation : | | |
| Organization: | | |
| Office Address : | | |
| Office Phone Number: | | |
| Email ID: | | |

12. Experience if any (in years and use additional sheet if required)

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

Signature

Note- If any information is required, extra A4 sheet may be attached.